

MC12019

Dual Modulus Prescaler

The MC12019 is a divide by 20 and 21 dual modulus prescaler. It will divide by 20 when the modulus control input is HIGH and divide by 21 when the modulus control input is LOW.

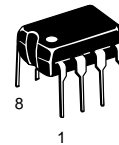
- 225 MHz Toggle Frequency
- Low-Power 7.5 mA Maximum at 5.5 V
- Control Input is Compatible with Standard Motorola CMOS Synthesizers
- Emitter Follower Output

MECL PLL COMPONENTS ÷20/21 DUAL MODULUS PRESCALER

SEMICONDUCTOR TECHNICAL DATA

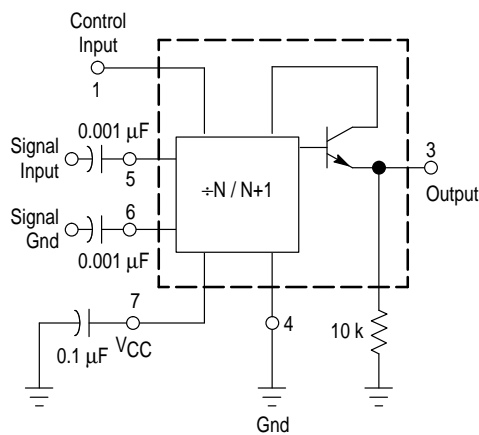


D SUFFIX
PLASTIC PACKAGE
CASE 751
(SO-8)

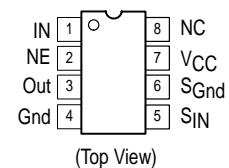


P SUFFIX
PLASTIC PACKAGE
CASE 626

SIMPLIFIED BLOCK DIAGRAM



PIN CONNECTIONS



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC12019D	$T_A = -40^\circ \text{ to } +85^\circ \text{C}$	SO-8
MC12019P		Plastic

MC12019

MAXIMUM RATINGS


Rating	Symbol	Value	Unit
Power Supply Voltage, Pin 7	V_{CC}	8.0	Vdc
Operating Temperature Range	T_A	-40 to +85	°C
Storage Temperature Range	T_{stg}	-65 to +175	°C

NOTE: ESD data available upon request.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 4.5$ to 5.5 V; $T_A = -40$ to 85°C), unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Toggle Frequency (Sine Wave Input)	f_{max} f_{min}	225 –	– –	– 20	MHz
Supply Current	I_{CC}	–	–	7.5	mA
Control Input HIGH (+20)	V_{IH}	2.0	–	–	V
Control Input LOW (+21)	V_{IL}	–	–	0.8	V
Output Swing Voltage (10 k Ω to ground)	V_{out}	600	–	1200	mVpp
Input Voltage Sensitivity 20 MHz to 225 MHz	V_{in}	200	–	800	mVpp
PLL Response Time (Notes 1 and 2)	t_{PLL}	–	–	t_{out-70}	ns

NOTES: 1. t_{PLL} = the period of time the PLL has from the prescaler rising output transition (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection.
2. t_{out} = period of output waveform.

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